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APPLICANTS

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** CONTINUING DATA *****
 This application is a DIV of 10/022,268 12/20/2001 *verified no*

** FOREIGN APPLICATIONS *****
 JAPAN 243274/2001 08/10/2001 *verified no*

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Rabin & Berdo</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 3
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TITLE
 Semiconductor package and method of fabricating same

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		<input type="checkbox"/> 1.16 Fees (Filing)
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